

AMENDMENTS TO THE SPECIFICATION

Please delete the section entitled "CROSS-REFRERNCE TO RELATED APPLICATIONS" in its entirety are substitute the following section therefor:

CROSS-REFERENCE TO RELATED APPLICATIONS

[0001] This application claims the benefit of the following U.S. Provisional Applications, which are herein incorporated by reference for all intents and purposes.

<u>SERIAL NUMBER</u>	<u>FILING DATE</u>	<u>TITLE</u>
60/464382 (NEXTIO.0103)	4/18/2003	SHARED-IO PCI COMPLIANT SWITCH
60/491314 (NEXTIO.0104)	7/30/2003	SHARED NIC BLOCK DIAGRAM
60/515558 (NEXTIO.0105)	10/29/2003	NEXIS
60/523522 (NEXTIO.0106)	11/19/2003	SWITCH FOR SHARED I/O FABRIC
60/541673 (NEXTIO.0107)	2/4/2004	PCI SHARED I/O WIRE LINE PROTOCOL

[0002] This application is a continuation-in-part of the following co-pending U.S. Patent Applications:

<u>SERIAL NUMBER</u>	<u>FILING DATE</u>	<u>TITLE</u>
10757713 (NEXTIO.0301)	1/14/2004	METHOD AND APPARATUS FOR SHARED I/O IN A LOAD/STORE FABRIC
10757711 (NEXTIO.0302)	1/14/2004	METHOD AND APPARATUS FOR SHARED I/O IN A LOAD/STORE FABRIC
10/757714 (NEXTIO.0300)	1/14/2004	METHOD AND APPARATUS FOR SHARED I/O IN A LOAD/STORE FABRIC

[0003] All of the aforementioned co-pending U.S. Patent Applications claim the benefit of the following U.S. Provisional Applications:

<u>SERIAL NUMBER</u>	<u>FILING DATE</u>	<u>TITLE</u>
<u>60/440788</u> <u>(NEXTIO.0101)</u>	<u>1/15/2003</u>	<u>SHARED IO ARCHITECTURE</u>
<u>60/440789</u> <u>(NEXTIO.0102)</u>	<u>1/21/2003</u>	<u>3GIO-XAUI COMBINED SWITCH</u>
<u>60/464382</u> <u>(NEXTIO.0103)</u>	<u>4/18/2003</u>	<u>SHARED-IO PCI COMPLIANT SWITCH</u>
<u>60/491314</u> <u>(NEXTIO.0104)</u>	<u>7/30/2003</u>	<u>SHARED NIC BLOCK DIAGRAM</u>
<u>60/515558</u> <u>(NEXTIO.0105)</u>	<u>10/29/2003</u>	<u>NEXIS</u>
<u>60/523522</u> <u>(NEXTIO.0106)</u>	<u>11/19/2003</u>	<u>SWITCH FOR SHARED I/O FABRIC</u>
<u>60/541673</u> <u>(NEXTIO.0107)</u>	<u>2/4/2004</u>	<u>PCI SHARED I/O WIRE LINE PROTOCOL</u>